

Adaptive Battery Equalization Algorithm for Capacitor-based Battery Management System

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Abstract In recent years, as energy-saving and environmental protection were widely concerned around the world, the lithium iron phosphate (LFP) battery has been widely used in hybrid electric vehicle (HEV) and electric vehicle (EV). Generally the battery pack for HEV is composition of the number of cells in series. In this case, imbalance among cells due to the difference of degradation and temperature will be accelerated by the cycles of charge and discharge without an appropriate battery equalization management system. However, the balancing system only intended to reduce the difference of cell voltage or Stage of Charge (SoC) before. “Adaptive Battery Equalization Algorithm for Capacitor-based Battery Management System” this thesis proposed re-explains the meaning of battery equalization. Instead of long-term using battery equalizer on standby, this thesis equalizes the battery when charging to save much spending time. It could not only adjust the balancing mechanism automatically in order to keep the available charge but also raise the average SoC to prevent the effect of battery aging and improve the cycle number of battery. Experiment results indicate that the available charge increases 1.5% and the battery capacity efficiently improves 7.3%.

Keyword Battery Equalization, Capacitor-based Battery Balance, LFP Battery, Battery Management System

1. Introduction

In recent years, lithium-ion batteries are widely used as an energy storage element and voltage source in many applications due to its high energy density, low

self-discharge rate, and high voltage each cell. Among the various types of lithium-ion batteries, the lithium iron phosphate (LFP) battery is the most popular because of safety, cost, and life span [1]. Because the applications demand high operating voltage than single cell voltage, a series connection of batteries is usually required to meet the voltage level.

However, repeatedly charge/discharge can cause battery imbalance due to the inevitable differences in battery electrochemistry characteristics caused by the variance of manufacturing environment [2]-[5]. The little variance will cause significant differences of battery characteristics, such as charge capacity, internal resistance, self-discharge rate, and etc. In addition, overcharging and over discharging of cells will cause irreversible damage or explosion. Therefore, battery should be used in appropriate region between the charging voltage limit (CVL) and the discharging voltage limit (DVL). The cell voltage should not get in the restricted area [6].

The cell voltage at restricted area will accelerate the battery degradation, so every cell should all be managed in appropriate voltage region. Fig. 1 shows the three charged and discharged series-connected cells. When one cell voltage is on CVL or DVL, the battery pack should stop charging/discharging immediately. There are many reasons why the capacity and internal resistance of the cells are not the same. Besides the manufacturing environment, the operating temperature is another reason. The capacity of whole battery stack is limited by weakest cell because it has lower capacity and higher internal resistance than others. Therefore, battery equalization is strongly required to maintain the available charge and lower the aging rate to extend the lifetime of the batteries [7].

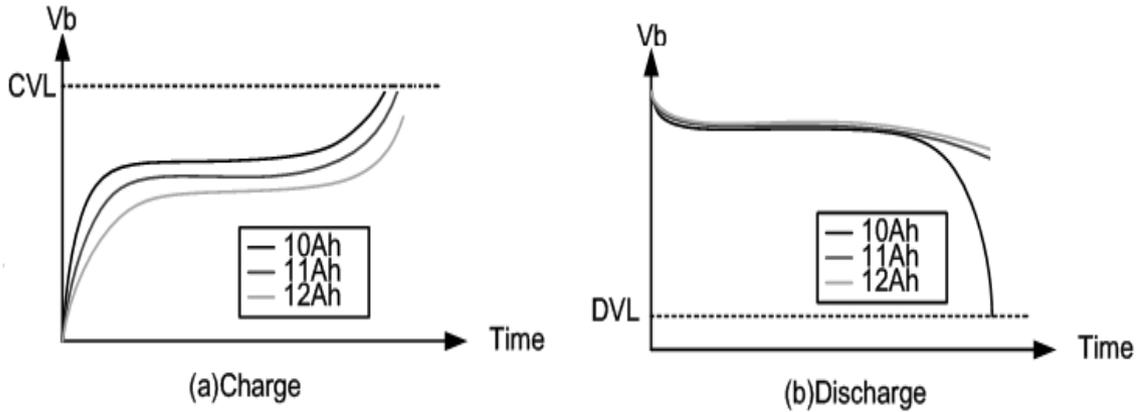


Figure 1. Cell voltages of a string battery pack.

Many techniques for cell equalization have been reported in previous research [8]-[10]. The simplest method is passive equalizer which uses parallel resistor to dissipate redundant energy through passive bypass route. However, with active cell equalization, charge can be transferred between the cells in the battery pack which uses a storage element like inductor, transformer, or capacitor. Among these equalization circuits, the switched capacitor is the promising method due to no bulky and large magnetic components. Thus, this thesis uses switched capacitor equalizer which is a power saving method as storage element [11].

In this paper, an adaptive battery equalization algorithm for capacitor-based battery management system has been proposed. The proposed method is operating when LFP battery is charging. The proposed battery equalization algorithm can not only save much balancing time but also adjust the equalization mechanism for different battery aging conditions to meet the valid and efficient results.

2. Adaptive Equalization Algorithm

Generally, the switched capacitor method is applied in lithium-ion battery when battery is standby. Cell balancing speed is slow in general because of the property of switched capacitor. The equalizer uses the difference of cells voltage to get the balancing current. When battery imbalance occurs, it balances SoC by the difference of EMF of different cells. This standby equalization method is workable for general lithium-ion battery because of its obvious change of EMF when SoC changes.

However, the conventional standby equalization is impractical for LFP battery. When the battery is standby, the open-circuit voltage (OCV) is nearly a constant form cell SoC is 40% to 80%. There is no significant change in OCV, so the balancing current from the difference of cell voltage is quite little. Therefore, the work of equalization must spend much more time to balance battery. Nevertheless, there is significant change in cell voltage when charge, so we try to equalize battery when battery is in charge.

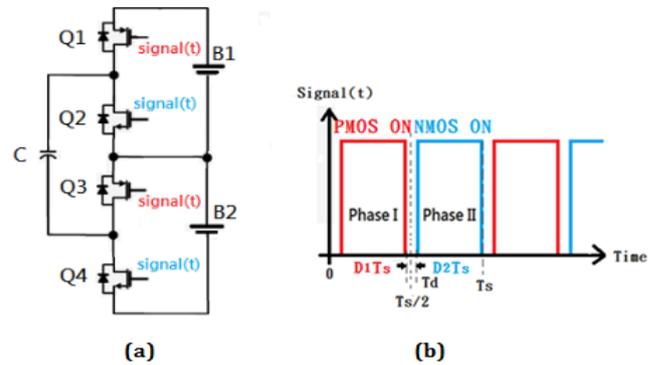


Figure 2. (a) Switch capacitor equalizer circuit. (b) Switching waveform.

Before researching the equalization mechanism, we analyze the switched capacitor equalizer. Fig. 2(a) shows the switched capacitor equalizer using two pairs of n-MOSFET and p-MOSFET to implement the Single Pole Double Throw (SPDT) switches which are connected to batteries. SPDT switches are alternately connected to upper side and lower side with a proper dead time. The switch function is shown as Fig. 2(b). In periodic steady state, V_c at the start of the charge cycle is equal to V_c at the end of the discharge cycle, V_{c2} and V_c at the start of the discharge cycle is equal to V_c at the end of the charge cycle, V_{c1} . We can get the equation:

$$V_{c1} - V_{c2} = (V_{b1} - V_{b2}) \times \frac{(1 - e^{-\frac{D_1 \times T_s}{\tau_1}})(1 - e^{-\frac{D_2 \times T_s}{\tau_2}})}{(1 - e^{-\frac{D_1 \times T_s}{\tau_1}}) \times e^{-\frac{D_2 \times T_s}{\tau_2}}} \quad (1)$$

where $\tau_1 = (R_1 + ESR) \times C$, $\tau_2 = (R_2 + ESR) \times C$, R_1 is the trace impedance in Phase I, R_2 is the trace impedance in Phase II, ESR is the equivalent series resistance of capacitor, T_s is period of switch, D_1 is the duty cycle of Phase I, and D_2 is the duty cycle of Phase II.

Then, the average balance current delivered by capacitor is:

$$i_b = \frac{V_{b1} - V_{b2}}{R_{eq}} \quad (2)$$

where

$$R_{eq} = \frac{1}{f_s \times C} \times \frac{e^{\left(\frac{D_1}{f_s \times C \times (R_1 + ESR)} + \frac{D_2}{f_s \times C \times (R_2 + ESR)}\right)} - 1}{\left(e^{\frac{D_1}{f_s \times C \times (R_1 + ESR)}} - 1\right) \times \left(e^{\frac{D_2}{f_s \times C \times (R_2 + ESR)}} - 1\right)} \quad (3)$$

Eq. (2) shows that the direction of balance current is decided by the difference of cell voltage. It is from the higher one to the lower one. Besides that, the other factor to effect balance current is Req. We can derive the limit of Req from Eq. (3) by using the L'Hospital's Rule:

$$\lim_{\frac{1}{f_s \times C} \rightarrow 0} R_{eq} = \frac{(ESR + R_1) + (ESR + R_2)}{D}, \text{ when } D_1 = D_2 = D. \quad (4)$$

Eq. (4) shows that Req has a limit. The simulations of charge balance without/with equalization are shown as Fig. 3(a) and (b). The new cell voltage is higher than aging one at CVL. It means that new cell got too much charge from the aging cell during charge. The results will decrease the available charge of battery because of the insufficient charge of aging cell.

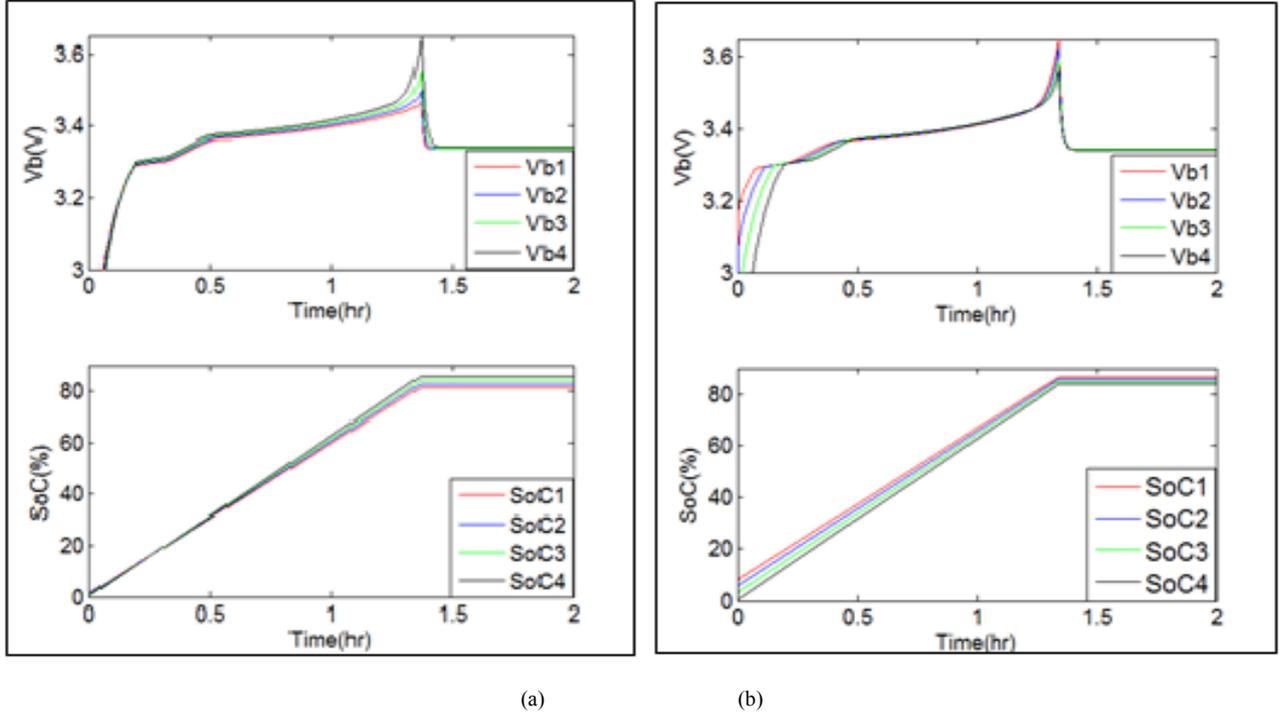


Figure 3. Simulation of 4-cell series-connected LFP battery (a) without equalization (b)with equalization.

Because of the long-term equalization during SoC in II stage, we should avoid equalization in this region. Therefore, the equalization should be triggered after leaving II stage. According to the slope of voltage in new and aging cell separately, the slope of voltage in aging one is larger than new one at II stage. That is to say, aging cell should trig the equalization later than new one which is shown as Fig. 4(a) and 4(b).

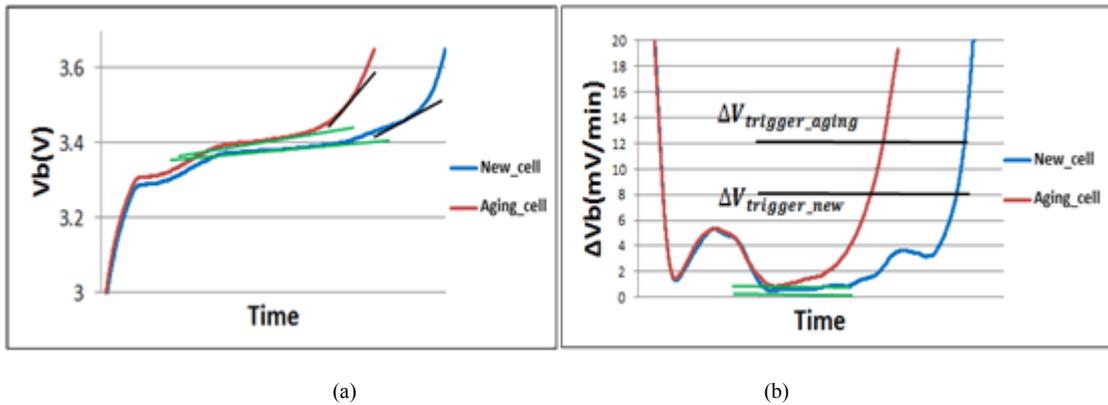


Figure 4. The charge characteristics of new cell and aging cell of (a) cell voltage and (b) cell voltage change rate.

Fig. 5 shows the equalization algorithm for adaptive tuning the equalization. The mechanism keeps sensing the cell voltage and computing the cell voltage of trigger by calculating the change rate of cell voltage slope. When meeting the trigger, it starts to trig the equalization. After meeting the CVL, it will record the voltage difference immediately and tune the trigger points and switched frequency at next cycle.

Comparing with standby balance method, the charge balance method can save much time in balance. If the equalization is triggered from charge beginning till the end, the results show that the available charge will decrease. Moreover, using the proposed algorithm can not only maximize the available charge but also efficiently charge both new and aging cells more charge.

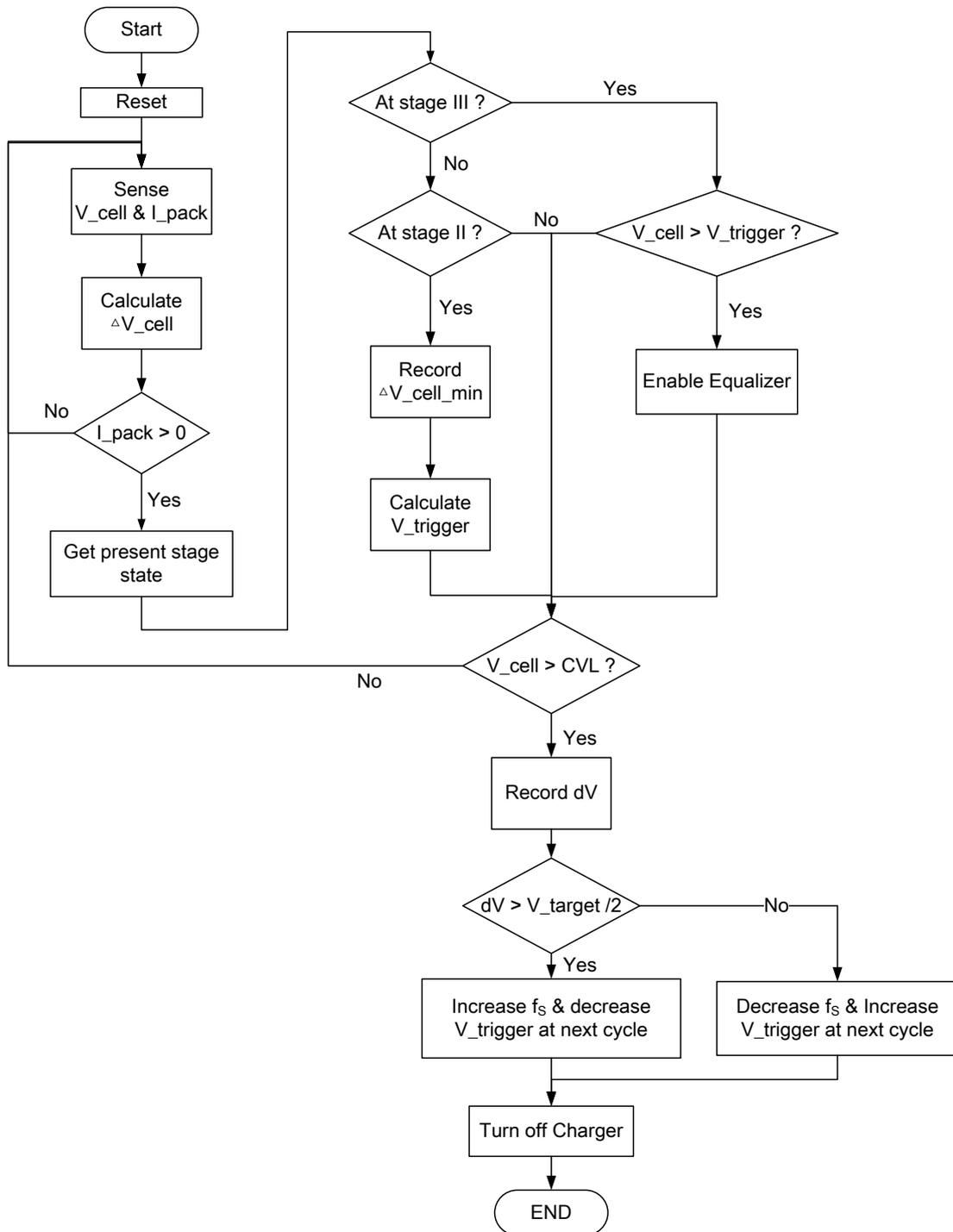


Figure 5. The adaptive battery equalization algorithm

3. Experimental Results

The overall adaptive equalization algorithm for capacitor-based battery management system is shown in Fig. 6. The monitor bq76PL536A keeps sensing 4 cells voltage and returns the information to control module through SPI. In control module, the master MCU 18F4580 controls the main behavior and the slave MCU 18F67K90 is in charge of output the PWM signals. After master MCU gets battery information from monitor, it will decide whether or not to switch off the charge route and compute the charge battery has gotten. If any cell voltage isn't higher than CVL, slave MCU would output PWM and EN signals to equalizers to equalize. Meanwhile, in order to get the whole information immediately, the BMS transmit data to PC through CAN-Bus at any time.

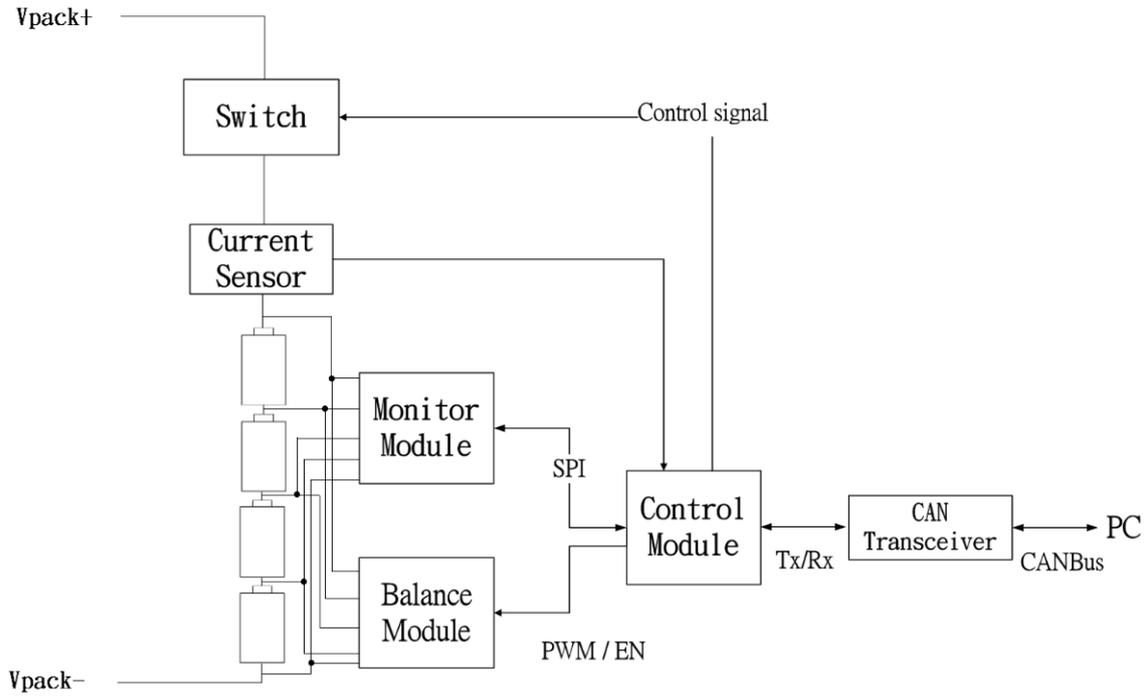


Figure 6. Block diagram of adaptive equalization algorithm for capacitor-based battery management system.

Table 1 shows the comparisons of three experiment results. Using the proposed algorithm can not only maximize the available charge or even increase 1.5% but also charge new 7.3% and aging cell 1.3% more respectively.

Table 1. Experiment results.

	Without balance	All balance	Proposed Algorithm
Q_{out}	36.5Ah	35Ah (-4.1%)	37.04Ah (+1.5%)
Q_{full} of new cell	9.34Ah	9.89Ah (+5.9%)	10.02Ah (+7.3%)
Q_{full} of aging cell	9.29Ah	8.96Ah (-3.6%)	9.41Ah (+1.3%)

4. Conclusions

An adaptive battery equalization algorithm for capacitor-based battery management system is proposed in this paper. For LFP battery, using capacitor equalizer in charge can save much balance time. Yet, the equalization in charge may decrease the battery performance without proper management. The proposed adaptive equalization algorithm

can adjust itself with the different aging conditions and tune trigger mechanism and the switched frequency at every cycle. The proposed BMS can not only raise the SoC of cells to lower the aging rate but also maximize the available charge to achieve beneficial results after equalization.

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