Different Gate Insulators for Organic Field Effect Transistors

O. Boughias¹²*, M.S. Belkaid¹, T. Trigaud², R. Zirmi¹

¹ Laboratory of Advanced Technology, Department of Electrical Engineering, Electronics, University of Mouloud Mammeri, Algeria
² X-LIM UMR 7252 laboratory, University of Limoges/ CNR, France

Abstract  The aim of this paper is to study the electrical properties of field effect transistors structures with two different insulators polymers, i.e., poly4, vinylphenol (PVP) and silicon oxide (SiO₂). In these studies, the organic active layer is always the same it is constituted of pentacene. PVP is an organic material with low k deposited by spin coating. Significant differences in terms of mobility and leakage current are observed with the two dielectrics. Mobility is almost 10 times higher with SiO₂ than with PVP. It is the same with the current leakage that are 1000 times smaller with SiO₂.

Keywords  Organic Field Effect Transistor, Pentacene, Silicon Oxide, Poly (4, Vinylphenol)

1. Introduction

The need of new devices more reliable and more powerful pushes development of the microelectronics. Among the different ways of this development, the use of organic materials takes an important place in the actual research technology. The organic electronic presents several advantages versus the classical electronic, among these advantages, the low cost of manufacturing process and the flexibility of its manufacturing support. The new technology does not seek to complete with silicon based electronics. However it can be an alternative for some applications. The aim is to improve the performance of electronic components. We have elaborated two series of organics transistors field effect. In the first one, the poly 4, vinylphenol (PVP) represents the gate insulator. In the second, the silicon oxide (SiO₂) is used as insulator gate. Electrical properties will be compared between the two series.

The insulator must have: high resistivity to prevent the leakage current between the gate metal and the semiconductor channel, and high dielectric constant to have enough capacitance for channel current flow⁴. There are organic and inorganic dielectric materials which fulfill this role. The functioning voltages observed with silicon oxide used as gate insulator is very high⁵. For this, we are interested to the organic insulators as polymethymetacrylte (PMMA) and ploy(4, vinylphenol) (PVP). These organics insulators should combine low functioning voltage and high stability. With PMMA like gate insulator we have high functioning voltage and highest stability⁶. The PVP is transparent insulator polymer in thin layer, with decomposition temperature neighbouring 330°C. It is soluble in TFH (Tetra Hydro Furan), in the dioxide and in fast alcohols (methanol, isopropanol, ethanol). The pentacene has greater holes mobility than polymers. This molecule allows the formation of polycrystalline thin films on smooth surface substrate⁷ and gives exceptional characteristics for thin film transistors⁸.

2. Structures Device and Fabrication of Field Effect Transistor

Field effect transistors with a composite layer based on insulator polymer, i.e., poly (4, vinylphenol) and silicon oxide were realized. The structure of organic field effect transistor is the classical invested staged configuration called top-contact, were the drain and source electrodes are deposited at the last step of the process on the pentacene.

Figure 1. Polymer molecule of PVP.⁸
The structure of the studied polymer molecule is shown in Fig. 1. In the first series, the study was carried out from a polymer insulator often used for organic transistors: PVP (poly 4 vinylphenol)~, where the relative dielectric permittivity is ~ 3.6. The insulator polymer PVP used in the present study were purchased in the Sigma-Aldrich and used with additional treatment. PVP was dissolved in isopropanol, the obtained solutions were then mixed, stirred and annealed at temperature of 50°C for 30 min. The proportion of the solution was 1g of PVP for 5ml of isopropanol, it is deposited by spin coater layer of the solution of polymer and then dried at 120°C for 1h30min. The layer thickness was estimated using a Bruker DEKTAK XT as ~ 1 µm. The metal gate is Indium Tin Oxide (ITO), with resistivity of ~ 10^{-4} \text{Ωcm}, etched with a heated hydrochloric acid, and it’s layer thickness as ~ 150 nm. The active layer thickness of pentacene as ~ 50 nm deposited using vacuum evaporator, and gold electrode source-drain deposited with the same technical. The layer thickness of electrode was ~ 50 nm, and the electrode width is ~ 4 mm.

In the second series, the active layer pentacene is applied on silicon (Si with a SiO_2 layer ~ 20 nm) substrates with thermally deposited Au electrodes. The silicon substrates were purchased in the Sigma-Aldrich. Figures 2a and 2b shows the structures top contact of field effect transistor with organic and inorganic polymers insulator.

Direct current- voltage characteristics of field effect transistor structures based on PVP and silicon oxide were measured in dark, in atmospheric vacuum at 300 K, in a voltage range from - 40 to +40 V, using an automated setup for the measuring \textit{I-V} characteristics, based on a Keithley-4200 SMU.

Figure 2. Structure of the top contact field effect transistor studied, (a) with PVP gate insulator and (b) silicon oxide as gate insulator.

Figure 3. Transient characteristics of field effect transistor with, (a) silicon (Si with a SiO_2 layer ~ 20 nm), channel length ~ 100 µm at \textit{V_{DS}} = - 40 V and (b) PVP insulator layer ~ 1µm, channel length ~ 100µm at \textit{V_{DS}} = - 15V.
3. Results and Discussion

Figures 4 and 5 show the $I-V$ characteristics of the field effect transistor with insulator organic layer PVP for the first series, and insulator silicon oxide inorganic layer for the second series. We observe an improvement of drain current, with the silicon oxide insulator compared with the use of insulator polymer PVP. The measured $I-V$ characteristics are typical of the unipolar hole field effect transistor operating under current saturation conditions at voltages slightly exceeding a certain threshold value $V_{th}$. The charge carrier mobility $\mu_{FET}$ of the active layer was estimated from the $I-V$ characteristic of the field effect transistor under saturation and weak-field conditions, respectively, from the relations\footnote{9,10}.

\begin{align}
I_D &= \frac{W}{2L} \mu_{FET} C_i (V_G - V_{th})^2 \tag{1} \\
I_D &= \frac{W}{L} \mu_{FET} C_i (V_G - V_{th}) V_{DS} \tag{2}
\end{align}

Where in our case $W=4\text{mm}$ is the channel width, $L=100\mu\text{m}$ is the channel length, $C_i$ is the capacitance per square area of the polymer insulator: for SiO$_2$ and for a thickness of $\sim 20$ nm, $C_i \approx 1.15 \times 10^{-9} \text{pF/cm}^2$ and for PVP with a thickness of $\sim 1 \mu\text{m}$, $C_i \approx 2.3 \text{nF/cm}^2$. $V_G$ is the gate voltage, and $V_{th}$ is threshold voltage corresponding to the accumulation mode beginning. For the field effect transistor based on the silicon oxide, the threshold voltage $V_{th} \sim -7 \text{ V}$ was estimated from the dependence of $I_{DS}$ on $V_{GS}$ at $V_{DS} = -40 \text{ V}$ (Fig. 3a) and the field effect transistor based on PVP, the threshold voltage $V_{th} \sim -18 \text{ V}$ was estimated on $V_{GS}$ at $V_{DS} = -15 \text{ V}$ (Fig. 3b). For a better comparison we decided to represent the same curve on the current-voltage characteristics based on the silicon oxide and PVP insulators polymers for fixed gate voltages. The Figure 6 shows the $I-V$ characteristic of the field effect transistor at gate voltage $V_{GS} = -30 \text{ V}$. The field effect mobility are $0.04 \text{ cm}^2\text{V}^{-1}\text{s}^{-1}$ with PVP as insulator and $0.3 \text{ cm}^2\text{V}^{-1}\text{s}^{-1}$ with silicon oxide. This difference is attributed to the high roughness of the PVP layer. One can also think that there are impurities at the interface PVP pentacene as leakage currents are higher with PVP the $I_{on}/I_{off}$ ratio was shown to be around $10^6$ with SiO$_2$ and $10^3$ with PVP.

4. Conclusions

The manufacture of completely organic transistor requires the use of polymer dielectric. These dielectric are often inefficient because it generates low mobility and significant leakage currents. These two features are related to the quality of the interfaces and to defects or impurities in the polymer. We have shown here that the performance were at least 10 times lower in the case of using organic insulator compared to usual used inorganic insulator as SiO$_2$.

REFERENCES


